

# Update Search

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L24	50096	memory near3 module\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/07 18:25
L25	4462	24 same (socket\$1 connector\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/07 18:15
L26	2008	25 same board\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/07 18:16
L28	413	26 same ((plurality multiple (first with second)) near5 module\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/07 18:34
L29	1	28 and "in series"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/07 18:22
L30	2	28 same (base adj (socket\$1 connector\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/07 18:22
L40	1797	(257/686).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/01/07 18:47
L41	1073	(257/691).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/01/07 18:47
L42	826	(257/693).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/01/07 18:47
L43	1811	(257/723).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/01/07 18:47
L44	320	(257/726).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/01/07 18:47
L45	675	(257/727).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/01/07 18:48

L46	538	(257/730).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/01/07 18:48
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# Interference Search

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L31	34987	(plurality multiple (first with second)) near5 module\$1	US-PGPUB	OR	OFF	2006/01/07 18:26
L32	2608	31 same (socket\$1 connector\$1)	US-PGPUB	OR	OFF	2006/01/07 18:28
L33	725	32 same (board\$1 PCB\$1)	US-PGPUB	OR	OFF	2006/01/07 18:27
L36	287	33 same (conductor\$1 wir\$3 electrode\$1 contact\$1)	US-PGPUB	OR	OFF	2006/01/07 18:31
L37	81	36.clm.	US-PGPUB	OR	OFF	2006/01/07 18:31


**PALM INTRANET**

Day : Saturday  
Date: 1/7/2006  
Time: 18:52:24

**Inventor Name Search Result**

Your Search was:

Last Name = CHOI

First Name = JUNG-HWAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09358068</a>	<a href="#">6181178</a>	150	07/21/1999	SYSTEMS AND METHODS FOR CORRECTING DUTY CYCLE DEVIATIONS IN CLOCK AND DATA SIGNALS	CHOI, JUNG-HWAN
<a href="#">09361862</a>	<a href="#">6584572</a>	150	07/27/1999	DATA INPUT-OUTPUT CIRCUITS THAT SELECTIVELY INVERT BITS	CHOI, JUNG-HWAN
<a href="#">09738311</a>	<a href="#">6492825</a>	150	12/18/2000	SOCKET AND PRINTED CIRCUIT BOARD FOR SEMICONDUCTOR DEVICE, AND METHOD OF TESTING SEMICONDUCTOR DEVICE	CHOI, JUNG-HWAN
<a href="#">09978497</a>	Not Issued	71	10/16/2001	Sockets for module extension and memory system using same	CHOI, JUNG-HWAN
<a href="#">10039357</a>	Not Issued	41	11/07/2001	Apparatus and method for thin film deposition	CHOI, JUNG-HWAN
<a href="#">10086675</a>	<a href="#">6590429</a>	150	02/28/2002	DATA RECEIVERS FOR REPRODUCING DATA INPUT SIGNALS AND METHODS FOR DETECTING DATA SIGNALS IN DATA INPUT RECEIVERS	CHOI, JUNG-HWAN
<a href="#">10166258</a>	<a href="#">6839786</a>	150	06/11/2002	INFORMATION PROCESSING SYSTEM WITH MEMORY MODULES OF A SERIAL BUS ARCHITECTURE	CHOI, JUNG-HWAN
<a href="#">10374304</a>	Not Issued	61	02/25/2003	Method for forming ruthenium film of a semiconductor device	CHOI, JUNG-HWAN
<a href="#">10379781</a>	Not Issued	41	03/04/2003	Simultaneous bidirectional input/output circuit	CHOI, JUNG-HWAN
<a href="#">10421533</a>	Not Issued	30	04/21/2003	Semiconductor device and method for testing the same	CHOI, JUNG-HWAN
<a href="#">10425824</a>	<a href="#">6831478</a>	150	04/30/2003	OPEN DRAIN TYPE OUTPUT BUFFER	CHOI, JUNG-HWAN
<a href="#">10641313</a>	<a href="#">6859078</a>	150	08/14/2003	DELAY LOCK LOOP CIRCUIT	CHOI, JUNG-HWAN
<a href="#">10645922</a>	Not	93	08/20/2003	MEMORY INTERFACE SYSTEM	CHOI, JUNG-HWAN

	Issued				
<u>10713533</u>	<u>6930513</u>	150	11/13/2003	SIMULTANEOUS BI-DIRECTIONAL SIGNAL TRANSMISSION SYSTEM AND SEMICONDUCTOR DEVICE THEREFOR	CHOI, JUNG-HWAN
<u>10719085</u>	Not Issued	30	11/21/2003	Simultaneous bi-directional buffer including self-test circuit having function of generating input signal and self testing method of the simultaneous bi-directional buffer	CHOI, JUNG-HWAN
<u>10750896</u>	Not Issued	30	01/05/2004	Control signal generation circuit and data transmission circuit having the same	CHOI, JUNG-HWAN
<u>10798469</u>	Not Issued	41	03/11/2004	Semiconductor memory device and method for writing and reading data	CHOI, JUNG-HWAN
<u>10811759</u>	<u>6856268</u>	150	03/29/2004	CONTROL SYSTEMS HAVING AN ANALOG CONTROL UNIT THAT GENERATES AN ANALOG VALUE RESPONSIVE TO A DIGITAL VALUE AND HAVING TWICE THE RESOLUTION OF THE LEAST SIGNIFICANT BIT OF THE DIGITAL VALUE AND METHODS OF OPERATING THE SAME	CHOI, JUNG-HWAN
<u>10819678</u>	Not Issued	93	04/06/2004	SEMICONDUCTOR DEVICE AND METHOD FOR INPUTTING/OUTPUTTING DATA SIMULTANEOUSLY THROUGH SINGLE PAD	CHOI, JUNG-HWAN
<u>10834062</u>	Not Issued	41	04/29/2004	Simultaneous bi-directional transceiver	CHOI, JUNG-HWAN
<u>10861038</u>	Not Issued	93	06/04/2004	SEMICONDUCTOR MEMORY DEVICE WITH A BYPASS CIRCUIT FOR VERIFYING THE CHARACTERISTICS OF AN INTERNAL CLOCK SIGNAL	CHOI, JUNG-HWAN
<u>10938534</u>	Not Issued	30	09/13/2004	Open drain type output buffer	CHOI, JUNG-HWAN
<u>10948498</u>	Not Issued	30	09/23/2004	Input/output line sense amplifiers and/or input/output line drivers in semiconductor memory devices and methods of operating such devices	CHOI, JUNG-HWAN
<u>10959114</u>	Not Issued	30	10/07/2004	Low power consumption semiconductor memory device capable of selectively changing input/output data width and data input/output method	CHOI, JUNG-HWAN
<u>11006981</u>	<u>6976848</u>	150	12/07/2004	SOCKETS FOR MODULE EXTENSION AND MEMORY SYSTEM USING SAME	CHOI, JUNG-HWAN

<a href="#">11019674</a>	Not Issued	20	12/23/2004	Memory module, method and memory system having the memory module	CHOI, JUNG-HWAN
<a href="#">11041660</a>	Not Issued	30	01/24/2005	Semiconductor devices having more than two-rows of pad structures and methods of fabricating the same	CHOI, JUNG-HWAN
<a href="#">11098818</a>	Not Issued	30	04/05/2005	Output drivers having adjustable swing widths during test mode operation	CHOI, JUNG-HWAN
<a href="#">11129272</a>	Not Issued	30	05/14/2005	Layout structure of signal driver	CHOI, JUNG-HWAN
<a href="#">11169393</a>	Not Issued	30	06/28/2005	Semiconductor device with improved signal transmission characteristics	CHOI, JUNG-HWAN
<a href="#">11226263</a>	Not Issued	30	09/15/2005	Pseudo differential output buffer, memory chip and memory system	CHOI, JUNG-HWAN
<a href="#">60305928</a>	Not Issued	159	07/16/2001	Data receiver using two reference signals	CHOI, JUNG-HWAN
<a href="#">60392731</a>	Not Issued	159	06/26/2002	Gene tagging in rice: a high throughput system for functional genomics	CHOI, JUNG-HWAN

Inventor Search Completed: No Records to Display.

<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>	<input type="button" value="Search"/>
	<input type="text" value="Choi"/>	<input type="text" value="Jung-Hwan"/>	

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